



FIG 1
(CONVENTIONAL)

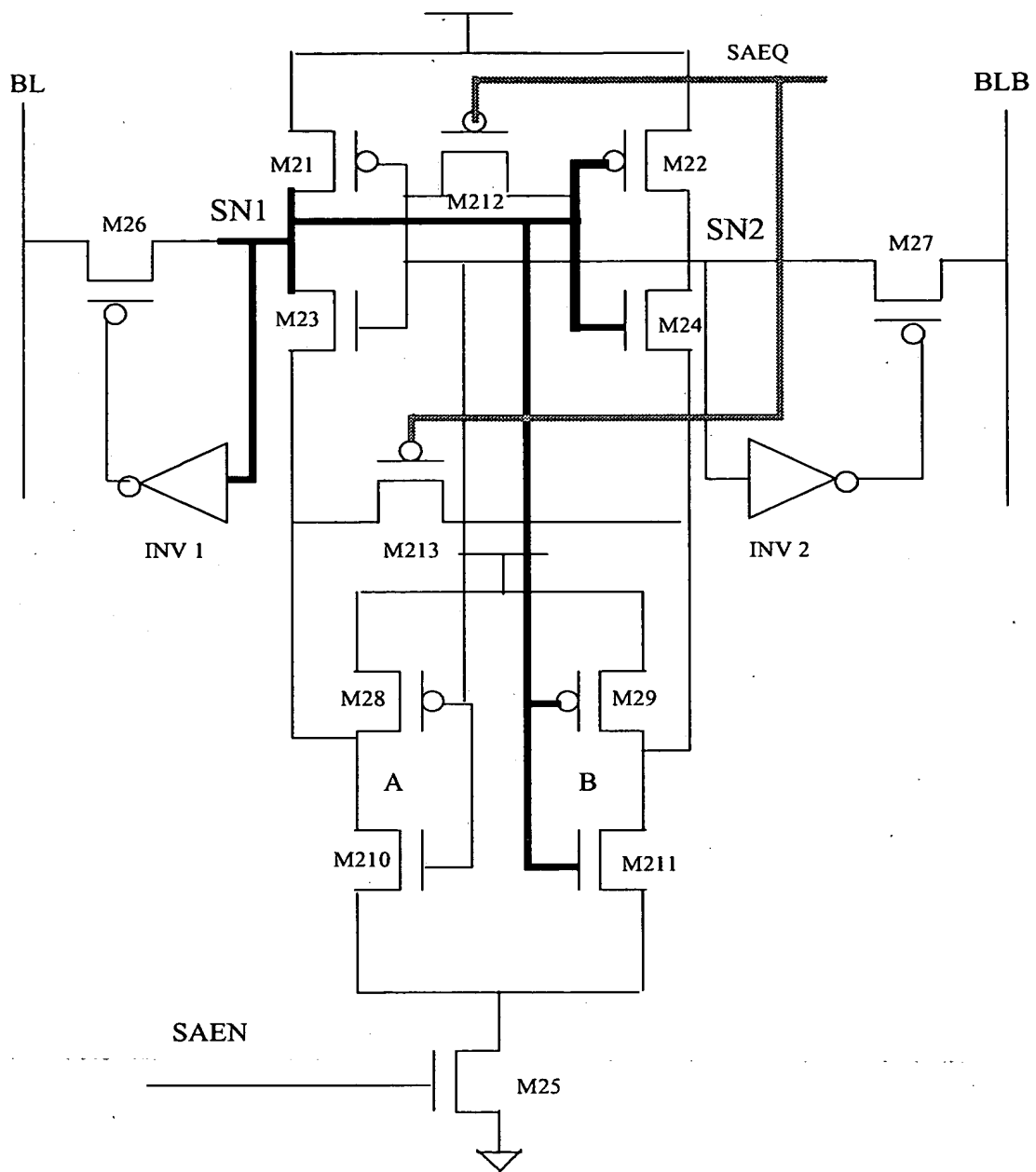


Figure-2
Proposed scheme with inverter feedback

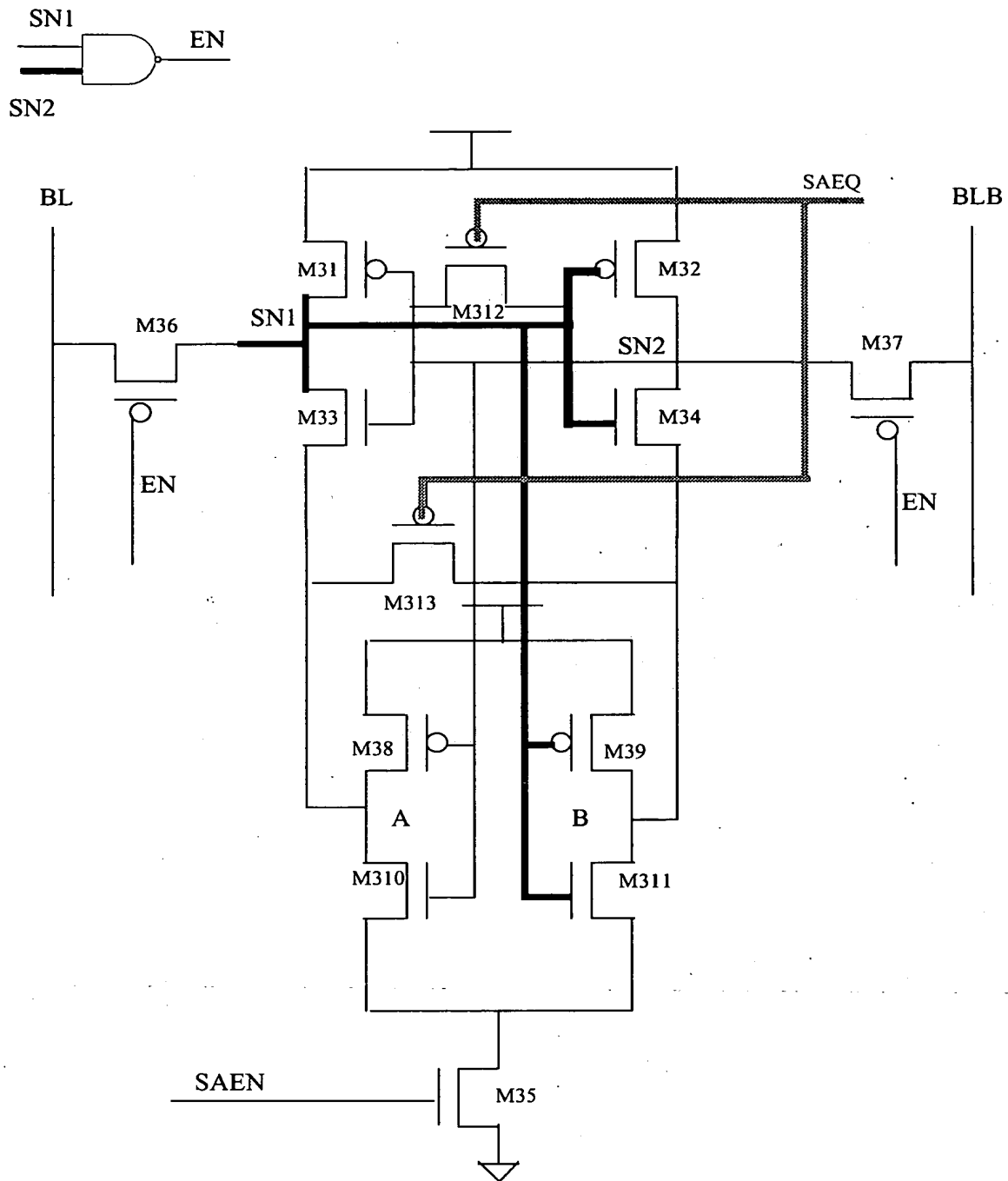


Figure-3
Proposed scheme with NAND feedback